# **DSG-NPS R&D Meeting Minutes**

**Date:** September 07, 2021 **Time:** 11:00 AM – 12:30 PM

<u>Attendees</u>: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

# 1. Reviewed 08/31/2021 meeting minutes

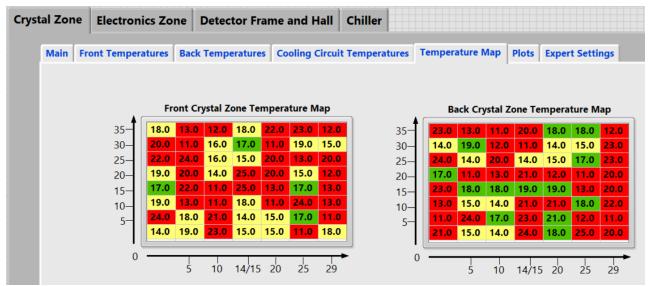
<u>DSG</u>

1. 08/31/2021 DSG-NPS R&D Meeting Minutes

## 2. Hardware Interlock System development

Mary Ann Antonioli, Peter Bonneau, Aaron Brown

- 1. Reviewed additions to *Temperature Map* tab for the LabVIEW Hardware Interlock Monitoring program
  - Back crystal zone temperature map has been added
  - "C" will be added to each map
  - An additional map indicating  $|T_{front}-T_{back}|$  will be added



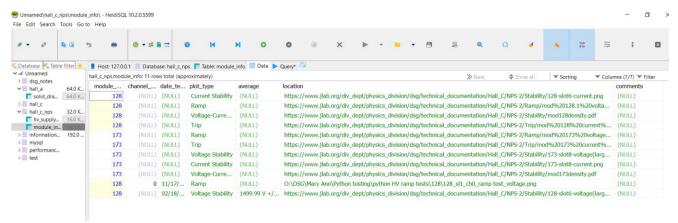
Screenshot of front and back crystal zone temperature maps for LabVIEW Hardware Interlock Monitoring program

2. Peter Bonneau and Aaron Brown will research direct communication with the Keysight switch/mainframe (bypassing LabVIEW) via a Python script

#### 3. Databasing

Aaron Brown, Pablo Campero, George Jacobs

- Reviewed first version of MySQL database, using HeidiSQL, for DSG's NPS drawings, notes, and talks
  - A separate average standard deviation column will be added to the *module\_info* table
  - An Excel spreadsheet will be developed detailing individual channel averages and standard deviations for module current and voltage stability tests



Screenshot of HeidiSQL module\_info database table

## 4. Thermal Analysis

Aaron Brown

- 1. Reviewed 3x3 block of PbWO<sub>4</sub> crystals with added carbon fiber dividers for the front of the crystal array
  - A new simulation will be conducted without both the carbon fiber dividers and the copper shell

# 5. High Voltage Supply Cable testing

Peter Bonneau, Aaron Brown, Brian Eng, George Jacobs, Mindy Leffel, and Marc McMullen

- 1. Mindy Leffel has the hot glue, glue gun, and enclosure (plastic box)
- 2. Brian Eng is writing the test procedure for the encapsulation test of the Radiall 52-pin connectors